

**Electronics Drafting**  
**CAD 1203**  
**Spring 2009**

**Instructor: Mary Smith**  
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**Text:** Electronics Drafting ('01 Revision), Frostad, Goodheart-Wilcox ISBN # 1566378583)

**Meeting Times:**

**Lecture: Monday 4:00pm – 5:30pm**

**Lab: Monday 5:55pm – 6:40pm**

**Course Description:** A course in techniques and general drafting with major emphasis on pictorial drawing, device symbol production drawings, flow and schematic diagrams, printed circuits, miniaturization, industrial controls, and graphic representation. Lecture, one hour per week; lab, two hours per week.

**Attendance is Required:** A student will be dropped after five unexcused absences. Roll will be taken at each session. Five extra credit points will be given to a student's final grade for perfect attendance.

**Assessment:** Students will be assessed with three tests and a final on their understanding of the vocabulary and standards for the industry. Lab assignments will be used to assess the students understanding of the industry standards that pertain to electronic drafting. The final grade of the student will be the sum of 35% of their lab assignments, 35% of their test average and 25% of their grade on the final.

**Grading Scale:**

**100 - 90 A**

**89 - 80 B**

**79 - 70 C**

**69 - 60 D**

**59 - 0 F**

**Classroom Policies:** Students are expected to respect others and the classroom setting. Please refer to the **Student Code of Conduct** as outlined in the **Student Handbook**

**Drop Policy:** Students wishing to drop the class will have to initiate the procedure. The instructor will not drop a student without student verification, unless student misses more than five classes. At the semester end, if a student has not dropped and has not completed the course requirements, a grade of F will be given. Final drop date is April 8, 2008. Please check with your counselor before dropping as it may affect your **Financial Aid** status.

**Special Needs:** If you are a student with a documented cognitive, physical or psychiatric disability you may be eligible for academic support services such as extended test time, texts on disc, notetaking services, etc... If you are interested in learning if you can receive these academic support

services, please contact either Tina Hardy (tina\_hardy@ivcc.edu, or 224-0284) or Judy Mika (224-0350), or stop by the Disability Services Office in B-204.

**Schedule of Assignments:**

<b>Note: This schedule is tentative, and may be modified during the course of the semester at the discretion of the Instructor</b>			
<b>Week</b>	<b>Topic</b>	<b>Reading</b>	<b>Assignments</b>
January 26	Electronic Drafting/ Block and Single Line Flow Diagrams	Pages 7 - 24	Page 29, Chapter 2- Activity 2
February 2	Electronic Symbols, Components and References	Pages 25 - 41	Pg 29, Review Questions, Activity 4 Pg 31)
February 9	Electronic Symbols, Components and References	Pages 41 - 60	Chapter 3, Activities 1 - 3
February 16	<b>President's Day/ College Closed</b>		
February 23	Review / <b>Test 1</b>	n.a.	Open Lab
March 2	Schematic and Logic Diagrams	Pages 63 - 70	Page 78, Review Questions
March 9	Schematic and Logic Diagrams	Pages 70 - 82	Chapter 4, Activity 8
March 16	Wiring Diagrams	Pages 83 -99	Chapter 5, Activity 3
March 23	<b>Spring Break - No Classes</b>		
March 30	Printed Circuitry	Pages 103 - 130	Chapter 6, Recreate the assembly drawing found on pg 122 to scale, and using the drawings found on pages 121 and 127 for reference
April 6	<b>Test #2 (Chapters 4 -6)</b>		Open lab
April 13	Printed Circuit Boards	Pages 131 - 142	Chapter 7, Review Questions
April 20	Enclosure Drawings	Pages 143 - 170	Chapter 8, Activity 1
April 27	Pictorial Drawings	Pages 171 - 185	Chapter 9, Review Questions
May 4	<b>Test #3 (Chapters 7 – 9)</b>		Open lab
May 11	<b>Final Exam</b>		